NEERAJ GANU

nganu@cs.stonybrook.edu https://www.linkedin.com/in/neeraj-ganu https://github.com/nvg24

EDUCATION Stony Brook University, Stony Brook, NY Master of Science, Computer Science GPA: 3.45/4 Courses: Operating Systems, Distributed Systems, Analysis of Algorithms, Artificial Intelligence Data Science Fundementals, Theorem of Databases, Network Lenguege Processing	May 2021 e, Data Visualization
Data Science Fundamentals, Theory of Databases, Natural Language Processing Vishwakarma Institute of Technology, University of Pune, India Bachelor of Technology, Computer Engineering GPA: 8.49/10	May 2019
WORK EXPERIENCE NVIDIA – CPU Architect Location: Hillsboro, Oregon • Working on analysis of architecture performance in simulation for NVIDIA CPU team.	July 2021 – Present
	lticore)
 SBU – Research Assistant April 2020 – May 2020 & Augu Analyse effects for task and data affinity for CPU threads in OpenMP (working with RV Implemented a library for GPU scheduling of OpenMP tasks Changes to LLVM - LIT for incorporating OpenMP multicore testing for existing tests i Create a Buildbot infrastructure to be able to run these tests on machines at the Exascalla Project was accepted to LLVM-ECP by DOE and will be extended to add GPU and Sch 	ust 2020 – May 2021 WTH Aachen) in LLVM test-suite ab at SBU
	st 2018 – December 2018 nel mode driver and an on- ation test submissions.
 SKILLS Programming Languages: C, C++, Python, Go, JavaScript, Java, C# Scripting in Linux and Windows, Windows/Linux debugging, Kernel level coding, gdb Experience with LLVM, CUDA, OpenMP, MPI, Machine Learning, Deep Learning, Bu 	uildbot
 PROJECTS Semantic Analysis of Comments Implemented a deep learning model (RNN with GRUs) to predict the nature of a comme Made use of text embeddings such as: GloVe, Word2Vec, FastText (ROC-AUC score o Built a telegram admin bot to monitor foul language in any telegram group. 	

Intermediate Code Generator for Compiler

- Given an expression or set of expressions generates forms of intermediate codes such as: three address codes, quadruples, triples, postfix, abstract syntax tree and directed acyclic graph
- Implemented the project using LEX and YACC. Other code in C++.

Spring 2019